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Impedance Reshaping Control Strategy for Improving Resonance Suppression Performance of a Series-Compensated Grid-Connected System

Haining Wang, Yandong Chen *, Wenhua Wu, Shuhan Liao, Zili Wang, Gaoxiang Li, Zhiwei Xie and Jian Guo

College of Electrical and Information Engineering, Hunan University, Changsha 410082, China; haining_wang@hnu.edu.cn (H.W.); wuwenhua168@hnu.edu.cn (W.W.); shuhanliao@hnu.edu.cn (S.L.); Ziliwang@hnu.edu.cn (Z.W.); lgxiang@hnu.edu.cn (G.L.); xiezhiwei@hnu.edu.cn (Z.X.); guojian@hnu.edu.cn (J.G.)

* Correspondence: yandong_chen@hnu.edu.cn; Tel.: +86-151-1626-8089

Abstract: In the series-compensated grid-connected system (SCGCS), there is an impedance interaction between the inverter impedance and the grid impedance that is prone to cause resonance in the SCGCS. In this paper, firstly, considering the effects of the phase-locked loop (PLL), currentloop, and frequency coupling, the broadband impedance model of the SCGCS is established. The stability of the SCGCS is analyzed by the impedance-based Nyquist stability criterion. It is found from the stability analysis that the impedance interaction between the inverter impedance and the grid impedance is the leading cause of the resonance. An impedance reshaping based resonance suppression method is proposed to suppress the resonance. The phase characteristics of the inverter equivalent output impedance are reshaped from the perspective of impedance. The phase margin at the intersection frequency of the inverter impedance and the grid impedance is improved. The proposed resonance suppression approach mainly consists of reshaping the current loop impedance and the novel phase-locked loop impedance. Finally, simulations and experiments are used to verify the feasibility of the resonance analysis and the effectiveness of the proposed control strategy.

Keywords: impedance reshaping; inverter; series compensation; resonance suppression

1. Introduction

With the rapid development of renewable energy generation [1] and smart grids [2] based on renewable energy sources (RES), many distributed generation (DG) systems are connected to the grid via power electronic interfacing converters. This not only solves the problem of a global energy shortage, but also brings new challenges to the safe operation of the grid at the same time. Power stations based on renewable energy are usually far away from big cities and are distributed in remote areas such as on islands and in deserts. Series compensation transmission lines are usually used in renewable energy power generation systems because they have the advantage of increasing transmission capacity [3,4].

The increase in renewable energy generation capacity may increase the risk of system oscillations [5], which will not only severely restrict the use of renewable energy, but even affect the stable operation of the system. Therefore, to solve these problems, the stability analysis of grid-connected inverters is very important for the safe operation of the entire grid system [6]. At present, the commonly used methods for analyzing the stability of grid-connected inverter systems mainly include impedance-based methods and state-space methods [7]. Compared with the state-space method, the impedance-based method only needs to establish an impedance model for each subsystem, and therefore, is simple and easy to implement, and is widely used to analyze the stability of the grid-connected inverters [8].

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Copyright: © 2021 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (http://creativecommons.org/licenses /by/4.0/). Establishing a comprehensive and accurate grid-connected inverter model is the basis for studying the mechanism of resonance. According to the different reference frames, impedance-based methods can mainly be divided into two types. One is the dq impedance model [9–11], and the other is the sequence impedance model [12]. The former is built using the synchronous reference frame, and the latter is built using the stationary reference frame. Reference [13] has proven that the sequence impedance model and the dq impedance model are equivalent when considering frequency coupling. It has become an essential method for analyzing inverter-based systems' stability because the sequence impedance model can be directly measured and has a simple form [12]. Therefore, the sequence impedance method is used to analyze resonance. However, in regard to the current research about the SCGCS oscillation problem, the impedance model established

does not consider the influence of frequency coupling. Since these resonance accidents occurred, research on the modeling and damping control strategies of the SCGCS has attracted more and more attention. However, when the grid-connected inverter adopts a quasi-proportional resonant (QPR) control, the oscillation problem caused by the impedance interaction of the series compensation system is rarely mentioned. Resonance may also appear on the connecting lines between multiple grid-connected systems [14], especially when the penetration rate of renewable energy reaches a certain level. This article focuses on mitigating the resonance caused by the impedance interaction between the inverter and the SCGCS. Additionally, by adding other equipment [15, 16], or improving the control strategy [17–21], it can also result in oscillation suppression, but in doing so will bring unnecessary energy loss. In this paper, the resonance was suppressed by impedance reshaping of the controller and did not require costly devices to avoid resonance. Experiments on the laboratory test platform and simulations on the full-scale system proved the effectiveness of the method at different compensation levels.

This paper established the broadband sequence impedance model and studied the stability of the SCGCS. It revealed that the resonance phenomenon between the inverter and the SGCCS is caused by impedance interaction, and proposed a control strategy based on impedance reshaping to suppress the resonance. The rest of the paper is organized as follows. In Section 2, the broadband impedance model of the grid-connected inverter will be derived based on the harmonic linearization theory, and the causes of resonance are analyzed. In Section 3, the impedance reshaping control strategy for resonance suppression is introduced, and the mechanisms of the proposed strategy for resonance suppression is analyzed. Experimental results are presented in Section 4. Section 5 concludes this article.

2. Modeling and Resonance Analysis of the SCGCS

2.1. The Broadband Impedance Model of the Inverter

Figure 1 shows the topology scheme of the SCGCS. v_{dc} represents the dc-side voltage in the grid-connected inverter mode. It is regarded as a constant value used in conjunction with energy storage equipment. e_a , e_b , and e_c are the inner electric potentials of the inverter; i_a , i_b , and i_c are the currents of filter inductance L_f ; u_a , u_b , and u_c are the point of common coupling (PCC) voltages; u_g is the grid voltages; L_f , C_f , and R_f represent the filter inductance, filter capacitance, and damping resistance, respectively; L_g , R_g , and C_g represent the grid inductance, resistance and capacitive series-compensated; Z_g presents the grid impedance, and Z_{inv} presents the impedance of the grid-connected inverter.



Figure 1. Configuration and control structure of the SCGCS.

The impedance model of the inverter that considers the effects of the PLL, the current-loop, and the frequency coupling is derived in this section using the harmonic linearization method. In order to obtain the impedance model of the inverter, it is necessary to inject a positive sequence disturbance voltage with a frequency of f_p at the PCC point. According to the frequency-coupling effects, injecting a positive-sequence disturbance voltage at the frequency f_p into the grid-connected inverter system will not only generate a positive-sequence response current at the frequency f_p , but also generate a response current at the coupling frequency f_p -2 f_1 [22]. The phase-A voltage of the inverter can be expressed in the time domain and the frequency domain as follows:

$$v_a(t) = V_1 \cos(2\pi f_1 t) + V_p \cos(2\pi f_p t + \varphi_{vp}) + V_{p2} \cos(2\pi f_{p1} t + \varphi_{vp1})$$
(1)

where V_1 is the amplitude of the fundamental voltage, V_p is the disturbance voltage at f_p , and V_{p2} is the voltage at f_{p1} , respectively; f_1 is the fundamental frequency; φ_{vp} is the initial phase angle of V_p , and φ_{vp2} is the initial phase angle of V_{p2} .

$$V_{a}[f] = \begin{cases} V_{\pm 1}, f = \pm f_{1} \\ ^{\wedge} v_{\pm p}, f = \pm f_{p} \\ ^{\wedge} v_{\pm p2}, f = \pm (f_{p} - 2f_{1}) \end{cases}$$
(2)

where $V_{\pm 1} = V_1/2$; $\hat{v}_{\pm p} = (V_p/2)e^{\pm j\varphi_{vp}}$; $\hat{v}_{\pm p2} = (V_{p2}/2)e^{\pm j\varphi_{vp2}}$.

$$I_{a}[f] = \begin{cases} I_{\pm 1}, f = \pm f_{1} \\ \stackrel{\wedge}{i_{\pm p}}, f = \pm f_{p} \\ \stackrel{\wedge}{i_{\pm p2}}, f = \pm (f_{p} - 2f_{1}) \end{cases}, M_{a}[f] = \begin{cases} M_{\pm 1}, f = \pm f_{1} \\ \stackrel{\wedge}{m_{\pm p}}, f = \pm f_{p} \\ \stackrel{\wedge}{m_{\pm p2}}, f = \pm (f_{p} - 2f_{1}) \end{cases}$$
(3)

where $I_{\pm 1}$ is the fundamental current, $\stackrel{\wedge}{i_{\pm p}}$ is the disturbance current response at f_p , and $\stackrel{\wedge}{i_{\pm p^2}}$ is the disturbance current response at f_{p_1} , respectively; $M_{\pm 1}$, $\stackrel{\wedge}{m}_{\pm p}$ and $\stackrel{\wedge}{m}_{\pm p^2}$ are modulating signals of phase-A.

According to the control diagram of the inverter, as the *abc*- $\alpha\beta$ is a linear transformation, it is not affected by the disturbance of the PLL. The frequency–domain expression of i_{α} and i_{β} can be obtained as follows:

$$I_{\alpha}[f] = \begin{cases} G_{i}(s)I_{\pm 1}, f = \pm f_{1} \\ G_{i}(s)\dot{i}_{\pm p}, f = \pm f_{p} \\ G_{i}(s)\dot{i}_{\pm p2}, f = \pm (f_{p} - 2f_{1}) \end{cases}, I_{\beta}[f] = \begin{cases} \mp jG_{i}(s)I_{\pm 1}, f = \pm f_{1} \\ \mp jG_{i}(s)\dot{i}_{\pm p}, f = \pm f_{p} \\ \pm jG_{i}(s)\dot{i}_{\pm p2}, f = \pm (f_{p} - 2f_{1}) \end{cases}$$
(4)

where $G_i(s) = 1/(1 + sT_i)$ represents the transfer function of the input current sampling filter and T_i is the sampling period of the current.

Furthermore, according to the small signal model of the phase-locked loop, we can get:

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$$\cos \theta_{PLL} = \begin{cases} \frac{1}{2}, & f = \pm f_1 \\ \frac{1}{2} T_{PLL}(s - j\omega_1) G_v(s) \left[\mathring{v}_{\pm p} - \mathring{v}_{\pm p2} \right], & f = \pm f_p \\ -\frac{1}{2} T_{PLL}(s + j\omega_1) G_v(s) \left[\mathring{v}_{\pm p} - \mathring{v}_{\pm p2} \right], & f = \pm (f_p - 2f_1) \end{cases}$$
(5)
$$\sin \theta_{PLL} = \begin{cases} \mp \frac{j}{2}, & \pm f_1 \\ \frac{1}{2} T_{PLL}(s - j\omega_1) G_v(s) \left[\mp j \mathring{v}_{\pm p} \pm j \mathring{v}_{\pm p2} \right], & f = \pm f_p \\ \frac{1}{2} T_{PLL}(s + j\omega_1) G_v(s) \left[\mp j \mathring{v}_{\pm p} \pm j \mathring{v}_{\pm p2} \right], & f = \pm (f_p - 2f_1) \end{cases}$$
(6)

where $G_v(s) = 1/(1 + sT_v)$, T_v is the sampling period of the voltage, $T_{PLL}(s) = H_{PLL1}(s)/(s + V_1H_{PLL1}(s))$, $H_{PLL1}(s) = k_{p_PLL} + k_{i_PLL}/s$ represents the proportional integral control of the PLL, and $\overset{\wedge}{v_{p2}}$ is the coupling-negative-sequence voltage perturbation.

Then, by using the frequency–domain convolution theorem, according to the control method of the grid-connected inverter, the $i_{\alpha r}$ and $i_{\beta r}$ expressed at the frequency–domain can be obtained with the following:

$$I_{\alpha r}[f] = \begin{cases} \frac{1}{2}(I_{dr} \pm jI_{qr}), & f = \pm f_{1} \\ \frac{1}{2}T(s - j\omega_{1})G_{v}(s)(I_{dr} \pm jI_{qr})[\hat{v}_{\pm p} - \hat{v}_{\pm p2}], & f = \pm f_{p} \\ -\frac{1}{2}T(s + j\omega_{1})G_{v}(s)(I_{dr} \mp jI_{qr})[\hat{v}_{\pm p} - \hat{v}_{\pm p2}], & f = \pm (f_{p} - 2f_{1}) \end{cases}$$

$$\left(\frac{1}{2}(I_{ar} \mp jI_{qr})[\hat{v}_{\pm p} - \hat{v}_{\pm p2}], & f = \pm f_{1} \end{cases}$$

$$(7)$$

$$I_{\beta r}[f] = \begin{cases} \frac{1}{2} (I_{qr} + jI_{dr}), & f = \pm f_1 \\ \frac{1}{2} T(s - j\omega_1) G_{\nu}(s) (I_{qr} \mp jI_{dr}) [\stackrel{\wedge}{\nu}_{\pm p} - \stackrel{\wedge}{\nu}_{\pm p2}], & f = \pm f_p \\ -\frac{1}{2} T(s + j\omega_1) G_{\nu}(s) (I_{qr} \pm jI_{dr}) [\stackrel{\wedge}{\nu}_{\pm p} - \stackrel{\wedge}{\nu}_{\pm p2}], & f = \pm (f_p - 2f_1) \end{cases}$$
(8)

Therefore, considering the voltage feedforward control and the current loop control, the phase-A modulation signal of the inverter can be obtained by (9).

$$m_{a}[f] = \begin{cases} \frac{1}{2}T(s-j\omega_{1})G_{v}(s)(I_{dr}\pm jI_{qr})[\hat{v}_{\pm p}-\hat{v}_{\pm p2}] - G_{i}(s)\hat{i}_{\pm p} \\ \frac{1}{2}T(s+j\omega_{1})G_{v}(s)(I_{dr}\mp jI_{qr})[\hat{v}_{\pm p}-\hat{v}_{\pm p2}] - G_{i}(s)\hat{i}_{\pm p2} \\ G_{PR}(s) + K_{f}(s)G_{v}(s)\hat{v}_{\pm p2}, \quad f = \pm(f_{p}-2f_{1}) \end{cases}$$
(9)

where $G_{PR}(s) = k_p + 2k_r\omega_c s/(s^2 + 2\omega_c s + \omega_g^2)$ represents the transfer function of the QPR, k_p and k_r are the proportional coefficient and resonance gain of the QPR controller, respectively, ω_c is the resonant bandwidth, and ω_g is the resonant frequency.

From Figure 1, the frequency–domain expression among the output currents, output voltages, and mid-point voltages can be obtained with (10).

$$L_f s \begin{bmatrix} I_a[f] \\ I_b[f] \\ I_c[f] \end{bmatrix} = K_m V_{dc} \begin{bmatrix} M_a[f] \\ M_b[f] \\ M_c[f] \end{bmatrix} - \begin{bmatrix} V_a[f] \\ V_b[f] \\ V_c[f] \end{bmatrix}$$
(10)

where $M_a[f]$, $M_b[f]$, and $M_c[f]$ are the modulating signals for the pulse width modulation (PWM), and K_m is the modulator gain. According to (3), (9), and (10), the admittance matrix of the inverter can be obtained with (11).

$$-\begin{bmatrix} \stackrel{\wedge}{i_p} \\ \stackrel{\wedge}{l_{p2}} \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{bmatrix} \begin{bmatrix} \stackrel{\wedge}{v_p} \\ \stackrel{\wedge}{v_{p2}} \end{bmatrix}$$
(11)

where Y_{11} and Y_{22} represent the sequence-admittances and Y_{12} and Y_{21} represent the coupling admittances. Therefore, according to (10) and (11), the sequence-admittance model expressed as MIMO can be derived. The expressions of Y_{11} , Y_{12} , Y_{21} , and Y_{22} are shown in (12) to (15).

$$Y_{11} = \frac{1 - \left[\frac{1}{2}T(s - j\omega_1)G_v(s)(I_{dr} + jI_{qr})G_{PR}(s) + K_f(s)G_v(s)\right]K_m V_{dc}}{G_i(s)G_{PR}(s)K_m V_{dc} + sL_f}$$
(12)

$$Y_{12} = \frac{\frac{1}{2}T(s - j\omega_1)G_{\nu}(s)(I_{dr} + jI_{qr})G_{PR}(s)K_m V_{dc}}{G_i(s)G_{PR}(s)K_m V_{dc} + sL_f}$$
(13)

$$Y_{21} = \frac{\frac{1}{2}T(s+j\omega_1)G_v(s)(I_{dr}-jI_{qr})G_{PR}(s)K_m V_{dc}}{G_i(s)G_{PR}(s)K_m V_{dc}+sL_f}$$
(14)

$$Y_{22} = \frac{1 - [\frac{1}{2}T(s + j\omega_1)G_{\nu}(s)(I_{dr} - jI_{qr})G_{PR}(s) + K_f(s)G_{\nu}(s)]K_m V_{dc}}{G_i(s)G_{PR}(s)K_m V_{dc} + sL_f}$$
(15)

Using the MIMO model, according to the generalized Nyquist criterion (NSC), the stability analyses can be carried out. However, to analyze the stability between the inverter and the grid, the model expressed as SISO is relatively convenient compared with the MIMO model. Furthermore, references [23,24] have verified that the SISO model can convert to the MIMO model equivalently. Then, the positive-sequence impedance can be obtained as follows:

$$Z_p(s) = [Y_{11}(s) - \frac{Y_{12}(s)Y_{21}(s)Z_g(s-2j\omega_1)}{1 + Z_g(s-2j\omega_1)Y_{22}(s)}]^{-1}$$
(16)

Similarly, according to the relationship between the positive- and negative-sequence impedance, the negative-sequence impedance can be calculated as:

$$Z_n(s) = Z_p^*(-s) \tag{17}$$

Table 1 shows the parameters of the grid-connected inverter. In order to verify the accuracy of the established inverter impedance model, at the impedance measurement simulation platform in Matlab/Simulink, it is assumed that the rated power of the inverter is 10 kW and is without a capacitive series-compensated system, and the grid-connected inverter operates stably. Figure 2 shows the impedance measurement results. As can be seen from Figure 2, the measurement results are very consistent with the established impedance model. The errors are minimal, which can explain the correctness of the established impedance model.

Parameters/Unit	Value	Parameters/Unit	Value
L_f (mH)	3	$C_{g}(\mathbf{F})$	0.0013
$C_f(\mu F)$	22	$\omega_c (rad/s)$	5
$R_f(\Omega)$	2.87	ω_g (rad/s)	100π
k_{p_PLL}	0.2659	L_g (mH)	15
k_{i_PLL}	10.9988	$R_g(\Omega)$	0.2
k_p	3	K_{f}	1/350
k_r	4	$\mathcal{V}_{dc}\left(\mathrm{V} ight)$	700
$V_1(\mathbf{V})$	311	$T_v(\mu s)$	50
<i>I</i> 1(A)	21.43	<i>Τi</i> (μs)	50
f_1 (Hz)	50	fs (kHz)	10

Table 1. Parameters of the grid-connected inverter.



Figure 2. Sequence impedance of the grid-connected inverter.

2.2. Effect of Parameters on Stability

According to Figure 1, in an interconnected system of inverters and power grids, the grid-connected inverter can be regarded as the Norton equivalent circuit of the ideal current source $I_m(s)$ parallel output admittance $Y_o(s)$. The grid provides the grid-connected voltage reference for the inverter at the PCC. The grid can be regarded as the Thevenin equivalent circuit of the impedance $Z_g(s)$ cascade with the ideal voltage source $V_g(s)$. The equivalent circuit of the grid-connected inverter system is shown in Figure 3.



Figure 3. Equivalent circuit of the SCGCS.

The output current of the connected nodes of the two subsystems can be deduced as follows:

$$I_0(s) = [I_m(s) - Y_0(s)V_g(s)] \frac{1}{1 + Y_0(s)Z_g(s)}$$
(18)

The stable operation of the grid-connected inverter system is the primary condition for access to the grid. Therefore, the expression $I_m(s) - Y_0(s)V_g(s)$ does not include the right half-plane poles. The stability of the output current and the voltage depends on the formula $1/[1 + Y_0(s)Z_g(s)]$. According to the automatic control theory, as long as the loop gain $Y_0(s)Z_g(s)$ of the closed-loop system without the right half-plane pole satisfies the Nyquist stability criterion, the inverter grid-connected system can maintain stability.

In order to analyze the stability of the system with different parameters, the impedance ratio of the SCGCS is established. Assuming that the grid is a completely symmetrical grid, $Z_{pg}(s) = Z_{ng}(s) = Z_g(s)$ is the expression of the positive- and negative-sequence impedances of the grid. In order to analyze the stability of the system, the impedance ratio of the inverter and the grid is defined as follows:

$$\begin{cases} G_p(s) = Z_g(s)/Z_p(s) \\ G_n(s) = Z_g(s)/Z_n(s) \end{cases}$$
(19)

According to (19) and the definition of series compensation level ($SCL = 1/(\omega_n^2 L_g C_g)$) [21], using different parameters, the Nyquist plots of (19) are shown in Figure 4.



Figure 4. Nyquist Plots of (19), (a). Different SCL, (b) different number N of parallel inverter.

As seen from Figure 4a, the Nyquist plots of $G_P(s)$ and $G_n(s)$ pass through the negative real axis to surround the point (-1, *j*0) from the right to the left when *SCL* increases from 25% to 50%. Therefore, the SCGCS gradually becomes unstable as the *SCL* increases. As the *SCL* gradually increases, the capacitive range of the grid impedance increases, which may lead to impedance interaction between the inverter impedance and the grid impedance. It can be seen from Figure 4b that when *SCL* = 25%, if the number of parallel inverters N gradually increases, the system gradually loses stability, and the Nyquist curves of $G_P(s)$ and $G_n(s)$ surround the point (-1, *j*0). From Figure 4, the increase in the number of parallel inverters and the increase in *SCL* will both make the stability of the SGCCS worse.

In order to further verify the correctness of the above analysis, according to the SGCCS shown in Figure 1, a simulation analysis was performed in MATLAB/Simulink, and the results are shown in Figure 5.



Figure 5. Simulation results with different *SCL*. (a) Grid-connected current waveform, (b) spectrum of the current when *SCL* = 50%.

When the *SCL* increases from 0 to 50%, it can be seen from Figure 5 that the SCGCS changes from a stable state to a resonance state, and the grid-connected current i_{gabc} shows resonance.

3. Impedance-Based Resonance Suppression Method

As shown in Figure 6, to mitigate the resonance between the inverter and the grid caused by the impedance interaction, an impedance-based resonance suppression method is proposed. The method is divided into two parts in reshaping the equivalent output impedance of the inverter. The first part is the impedance reshaping of the PLL, and the second part is the impedance reshaping of the current loop. A detailed analysis and design are as follows, where K_{prom} is the modulator gain, m_{abc} is the modulating signal, i_{gabc} is the current of the grid, and i_{abc} is the output current of the inverter:



Figure 6. Proposed resonance suppression method based on impedance reshaping.

3.1. Phase-Locked Loop Impedance Reshaping

In the low frequency band, the equivalent additional impedance of the PLL is negative and will have an adverse effect on the stability of the system. In order to reduce the impact of the negative impedance on the stability of the system, an effective measure is to increase the PLL order to correct the phase-frequency characteristics of the additional impedance of the PLL. This paper proposes a cascaded phase-locked loop (CPLL) structure to reshape the impedance of the inverter. Figure 7 shows the control block diagram of the CPLL.



Figure 7. Control block diagram of the CPLL.

According to Figure 7, combined with the traditional SRF-PLL transfer function, the closed-loop transfer function of the CPLL can be obtained and is expressed as follows:

$$T_{PL}^{*}(s) = V_{1}T_{PL}(s)\frac{H_{PLL2}(s)}{(s + V_{1}H_{PLL2}(s))}$$
(20)

where $T_{PL}(s)$ is the closed-loop transfer function of the traditional SRF-PLL.

After reshaping the impedance of the PLL, when the CPLL system control parameters are the same as the traditional SRF-PLL system parameters, $K_m = 0.8$, the frequency characteristic curve of the positive sequence impedance of the inverter can be shown, as seen in Figure 8.



Figure 8. Impedance characteristic curve of the inverter after PLL impedance reshaping.

As seen in Figure 8, compared to the SRF-PLL, the inverter impedance amplitude is slightly increased. The phase difference between the inverter impedance and the grid impedance decreases obviously. The stability margin of the grid-connected inverter using CPLL is improved. Therefore, the stability of the grid-connected system is enhanced by using the CPLL.

3.2. Current Loop Impedance Reshaping

A phase compensation control method adds to the current loop to improve the phase margin of the system. This compensates for the lack of phase margin caused by the power grid weakening. The control block diagram is shown in Figure 9.



Figure 9. Proposed current loop impedance reshaping control strategy.

As seen from Figure 9, the phase compensation loop is in series with the current loop. It can be expressed as follows:

$$G_m(s) = k_s \frac{1 + \xi_1 s}{1 + \xi_1 \xi_2 s}$$
(21)

where k_s is the gain coefficient of the phase angle compensation, ξ_1 is the phase angle coefficient of the phase angle compensation, and ξ_2 is the proportional coefficient of the phase angle compensation. The expression of the phase frequency function of $G_m(s)$ is:

$$\varphi_m(\omega) = -\arctan\left[\frac{(\xi_2 - 1)\xi_1\omega}{1 + \xi_1^2\xi_2\omega^2}\right]$$
(22)

Assuming that the angular frequency of the maximum compensation phase of $\varphi_m(\omega)$ is selected at f_{cw} , we can get:

$$\xi_2 = \frac{1}{\xi_1^2 (2\pi f_{cw})^2} \tag{23}$$

Assuming that the maximum compensation phase is ω_c , combining Equations (22) and (23), the expression of ξ_1 can be obtained as:

$$\xi_1 = \frac{\tan \omega_c + \sqrt{1 + \tan \omega_c^2}}{2\pi f_{cw}} \tag{24}$$

In this article, we use values $f_{ew} = 120$ Hz and $\omega_c = 45^\circ$, so that $\xi_1 = 0.0032$ and $\xi_2 = 0.1719$ can be calculated. In order to facilitate the attenuation of high-frequency harmonic components, the value of k_s cannot be too large, but equally, if the value of k_s is very small, it will lead to a decrease in the gain of the system at the fundamental wave frequency and increase the tracking error of the current. Therefore, k_s needs be a compromise value. Based on the above analysis, $k_s = 0.4$ is chosen in this paper. The positive-sequence impedance characteristic curve of the inverter when the SRF-PLL is used, before and after the current loop impedance reshaping, is shown in Figure 10.



Figure 10. Impedance characteristic curve of the inverter after the current loop impedance reshaping.

As seen in Figure 10, after the current loop impedance reshaping, in the low-frequency region, the phase decreases, and the amplitude of the impedance remains the same. Therefore, due to the increase in the system phase angle margin, the stability of the grid-connected system is improved.

According to (12)–(17), (20), and (21), after adding the proposed two parts of impedance reshaping, the new impedance ratio can be expressed as follows:

$$\begin{cases} G_{pc}(s) = Z_g(s)/Z_{pc}(s) \\ G_{nc}(s) = Z_g(s)/Z_{nc}(s) \end{cases}$$

$$\tag{25}$$

where $Z_{pc}(s)$ and $Z_{nc}(s)$ are the positive-sequence and negative-sequence impedances of the inverter after impedance reshaping, respectively.

According to (25), the Nyquist plots of (25) after adding the impedance reshaping control are depicted in Figure 11 when the SCL = 50%.



Figure 11. Nyquist Plots of (25) after adding the impedance reshaping.

As seen in Figure 11, after the impedance reshaping, the Nyquist plots of $G_{pc}(s)$ and $G_{nc}(s)$ do not encircle the (-1, *j*0) point. Therefore, when the *SCL* = 50%, by adding the impedance reshaping control strategy, the SCGCS changes from an unstable state to a stable state.

Aiming at the resonance phenomenon of the SCGCS in Figure 1, in order to prove the effectiveness of the proposed impedance reshaping control strategy in suppressing resonance, the simulation results obtained by MATLAB/Simulink when the SCL = 50% are shown as Figure 12.

As seen from Figure 12, When the power is 10 kW and 20 kW, with the proposed impedance reshaping control, the resonance can be effectively suppressed, and the SCGCS can maintain stability at last.



Figure 12. The grid-connected current waveform after the impedance reshaping. (a) P = 10 kW. (b) P = 20 kW.

4. Experimental Results

To verify the validity of the proposed method, the SCGCS and the impedance measurement equipment are built, as shown in Figure 13. The digital signal processor (DSP) + FPGA solution is selected as the control core of the system, and the execution of the control algorithm is realized by DSP TMS320F2812. Use EP2C8Q208CN to realize the field programmable gate array (FPGA) current and the voltage signal collection. The parameters of the circuit and controllers are given in Table 1. The experimental results are shown as follows:



Figure 13. Experimental platform. (a) The SCGCS, (b) impedance measurement equipment.

Experimental validation of the developed models is shown in Figure 14. Figure 14a,b show the positive-sequence impedance and the negative-sequence impedance of the SCGCS before and after the impedance reshaping, respectively. As can be seen from Figure 14, the measurement results are very consistent with the established impedance model as the errors are minimal. The impedance of the inverter after the impedance reshaping becomes more resistive below the fundamental frequency, and at the frequency where the amplitude curve intersects, the phase difference between the inverter impedance and the grid impedance becomes less than 180° ($\varphi_{3,4} < 180^{\circ} < \varphi_1$), which means that the system will change from an unstable state to a stable state.



Figure 14. Experimental results of the impedance measurement (a). Before impedance reshaping control, (b) after the impedance reshaping control.

In order to analyze the stability of the system when using different control methods under different *SCL*, four cases are selected as experimental verifications, as shown in Table 2.

Case	Implementation Form
Case A	Without the impedance reshaping when $SCL = 100\%$
Case B	With the impedance reshaping when $SCL = 100\%$
Case C	Without the impedance reshaping when SCL = 50%
Case D	With the impedance reshaping when $SCL = 50\%$

Table 2. Four cases under different control methods.

Figure 15 shows the experimental waveforms with no impedance reshaping when SCL = 100%, the grid-connected system loses stability, the THD of the grid current is 281.14%, which results from the impedance interaction between the inverter and the grid.

Figure 16 shows the experimental waveforms with the impedance reshaping when SCL = 100%. As seen in Figure 16, after the impedance reshaping, the impedance of the power grid and the inverter does not meet the conditions of resonance generation. The resonance phenomenon disappeared, and the system is in a stable state.

Figure 17 shows the experimental waveforms with no impedance reshaping when SCL = 50%. The THD of the grid current is 135.72%. Compared with Figure 15, the THD of the grid current is decreased.

Figure 18 shows the experimental waveforms with the impedance reshaping when *SCL* = 50%. As seen in Figure 18, after the impedance reshaping, the resonance phenomenon disappeared. By using the proposed impedance shaping control strategy, the resonance caused by the interaction between the inverter impedance and the grid impedance is effectively suppressed. The above analysis results show that the experimental results are basically consistent with the theoretical analysis, and prove the effectiveness of the proposed resonance suppression strategy.



Figure 15. Experiment results with no impedance reshaping control strategy when *SCL* =100%. (**a**) Grid current, (**b**) PCC voltage, (**c**) active and reactive power.



Figure 16. Experiment results with the impedance reshaping control strategy when *SCL* =100%. (**a**) Grid current, (**b**) PCC voltage, (**c**) active and reactive power.



Figure 17. Experiment results with no impedance reshaping control strategy when *SCL* =50%. (a) Grid current, (b) PCC voltage, (c) active and reactive power.



Figure 18. Experiment results with the impedance reshaping control strategy when *SCL* = 50%. (**a**) Grid current, (**b**) PCC voltage, (**c**) active and reactive power.

5. Conclusions

In order to solve the resonance problem caused by the impedance interaction between the inverter impedance and the grid impedance in the series compensation system, this paper proposes an impedance reshaping control strategy to suppress this resonance. The conclusions are shown as follows:

- (1) The SCGCS has similar power resonance problems as the traditional SG-based SCGCS. With the increase in *SCL*, power resonance will appear in the SCGCS. According to the established impedance model, the impedance of the inverter is inductive near the fundamental frequency. The impedance of the series-compensated network near fundamental frequency was capacitive, so it was prone to couple with the inverter, easily causing resonance. Additionally, with the increase in the *SCL* and the number of parallel inverters, the stability of the SCGCS worsens.
- (2) In order to suppress the power resonance of the SCGCS, an impedance reshaping control strategy is proposed and verified with experiments. The amplitude–frequency characteristics of the inverter impedance are changed through the PLL and the current loop impedance shaping. After the impedance reshaping, the SCGCS can maintain a sufficient phase margin, and the stable operation of the SCGCS is guaranteed.
- (3) The proposed impedance shaping control can be easily applied to other controllers to achieve a good resonance suppression effect, and no additional resonance suppression equipment is needed, which reduces equipment investment and power loss.

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