



receivers to transmit or receive data on any channel. When the optical signal arrives at FDL, it will be delayed a fixed interval for the address recognition processing, and then may be dropped by adjusting the SOA switching if necessary. The guard time ( $T_g$ ) distance between two neighboring burst packets should be long enough to cover the maximum switching time of switch ( $T_s$ ) [3] or the wavelength time ( $T_w$ ) of tunable transmitter [4] for avoiding burst packet collision when burst packet was added or dropped.

### ● Burst Assembly Algorithm

Two challenges in designing an OBS network are the burst assembly size and the burst assembly time, which reduces the switching burden and overheads at the optical layer. A typical IP packet is too small to efficiently process for optical networks. The necessary overhead would lead to very low bandwidth utilization. Therefore, incoming packets have to be grouped into bigger burst packet, but the impacts on the burst assembly size at heavy load and the burst assembly time at light load are worthy to study for variable-size packets. Packets wait for transmitting are organized into transmit queues according to their destination. In this paper, the transmit queues are served in longest queue first (LQF) that the highest priority is given to the longest burst queue. Although the burst assembly can reduce the switching burden and overheads at the optical layer, but two possible problems occur in light load and heavy load. First, the burst assembly may be too long before the burst reaches its minimum size limit  $B_{min}$  at light load. It can be improved by using the time-out scheme. Second, bursts will be smaller than the maximum burst size limit  $B_{max}$  at heavy loads when time-out  $T_{max}$  is too small. Therefore, in this paper, we propose a novel to solve above two problems. First we study maximum throughput ( $P_{max}$ ) based on burst assembly size to find burst ranges  $B_{min}$  to  $B_{max}$  at heavy load a timeout  $T_{max}$  can be used to avoid a long packet delays at light load based on  $B_{max}$  that burst assembly sizes cannot smaller than the  $B_{max}$  size at heavy load. So, we defined the timeout  $T_{max}$  as follow:

$$T_{max} = \frac{B_{max}}{(N-1) \times P_{max}} \quad (1)$$

where

$P_{max}$ : the average maximum throughput per node at heavy load

Therefore, in order to improve the bandwidth under guard time effect. In this paper, the FDL length must to be equal  $B_{max} + T_g$ .

$$FDL = B_{max} + T_g \quad (2)$$

### ● Simulation and Results

The simulations are done by the SIMSCRIPT II codes. The parameters of simulation are listed in Table 1. The queue length of transmit queues is finite. In all simulations, we assume that:

- (1) The  $P_{max}$  selected a turning point of simulation curves shown as fig. 2. This turning point represents high performance and low transmission delay.
- (2) The packets arrive according to a Poisson process and the arrival rate of the packets at each node is the same. Variable-size packets with a cumulative distribution function measured at Intel data center [5], but the packets would be fragmented when the packet's length larger than 1500 Bytes. Therefore, the mean packet size for this distribution is 353.8 Bytes.

All  $P_{max}$  in fig. 3 are approached to 8.47 Gb/s under the condition  $T_g = 0 \sim 100$ ns with  $W=4$  and  $N=8$ , and the  $P_{max}$  will be 10 Gb/s when  $W=N=8$  except the worst-case of  $T_g=100$ ns and  $B_{min}$  or  $B_{max}$  too short as shown in fig. 4.; in the case,  $P_{max}$  approaches to 9.4 Gb/s only. The results show the proposed burst algorithm provides excellent performance. By the above simulations, the upper bound of  $B_{min}$  and the lower bound of  $B_{max}$  under different  $T_g$  had found in Table 2. Afterward, the length of FDL and  $T_{max}$  can be calculated in Table 3 by formula (2). Furthermore, more simulations using the FDL in table 3 obtained the average end-to-end transmission delay ( $T_d$ ) under different  $T_g$  with  $W=4$  and  $N=8$ , and the results are shown as in fig. 5. The figure shows that  $T_d$  has a large value at lighter load; hence the table 4 is derived by formula (1) to solve the problem. In fig. 6, the  $T_d$  had decremented at lighter load, and did not affect  $T_d$  at heavy load.

**Table 1. The simulation parameters.**

| Node Architecture : TT-FR <sup>W</sup> |                       |
|--|-----------------------|
| Number of Nodes                        | 8 (Fixed)             |
| Number of Channels                     | 4, 8                  |
| Light Velocity                         | $2 \times 10^8$ m/s   |
| Ring Network Length                    | 100 km                |
| Channel Speed                          | 10 Gb/s               |
| Queue Length                           | $10^5$ Bytes          |
| Guard-time (i.e. $T_g$ )               | 0ns, 4ns, 10ns, 100ns |
| $B_{min}$                              | 40~1,000 Bytes        |
| $B_{max}$                              | 1,000~30,000 Bytes    |

**Table 2. The  $B_{min,up-bound}$  and  $B_{max,low-bound}$  under different  $T_g$  with  $W=4$  and  $N=8$ .**

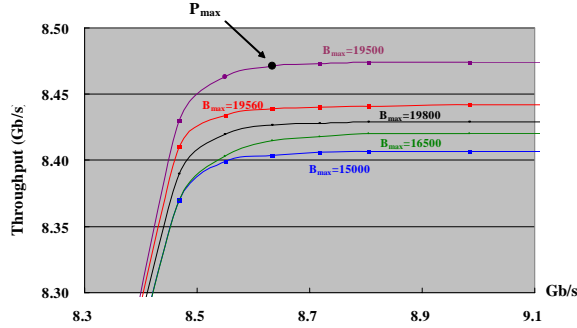
| $T_g$                       | 0ns   | 4ns   | 10ns  | 100ns |
|-----------------------------|-------|-------|-------|-------|
| $B_{min,up-bound}$ (Bytes)  | 40    | 280   | 340   | 800   |
| $B_{max,low-bound}$ (Bytes) | 22500 | 25500 | 27000 | 28500 |

**Table 3. The superior FDL length based on  $B_{max,low-bound}$  under different  $T_g$  with  $W=4$  and  $N=8$ .**

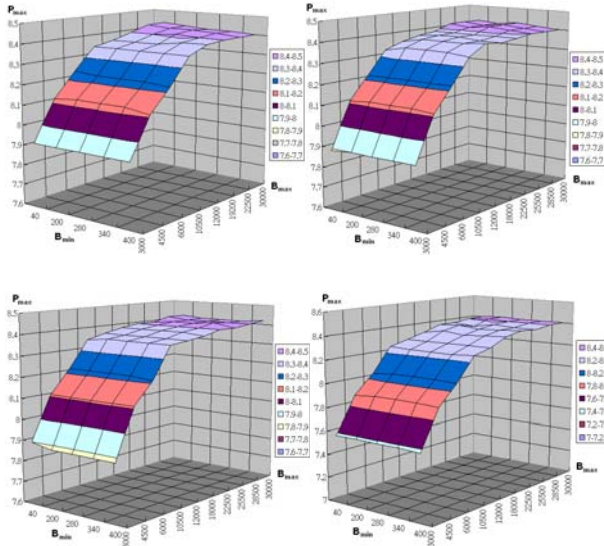
| $T_g$                           | 0ns   | 4ns   | 10ns  | 100ns |
|---------------------------------|-------|-------|-------|-------|
| FDL <sub>Superior</sub> (Bytes) | 22500 | 25505 | 27013 | 28625 |

**Table 4. The  $T_{max}$  based on  $B_{max,low-bound}$  under different  $T_g$  with  $W=4$  and  $N=8$ .**

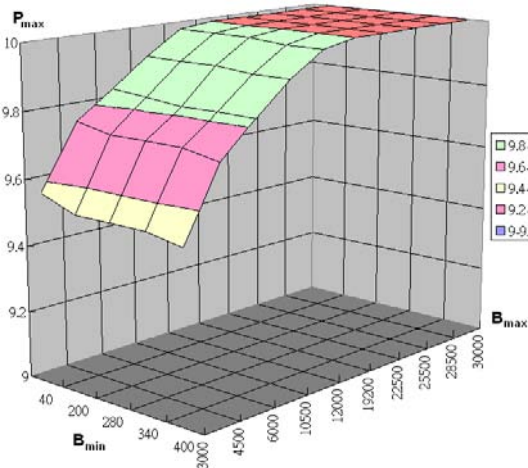
| $T_g$         | 0ns    | 4ns    | 10ns   | 100ns  |
|---------------|--------|--------|--------|--------|
| $T_{max}(ns)$ | 3035.9 | 3441.4 | 3645.9 | 3862.4 |



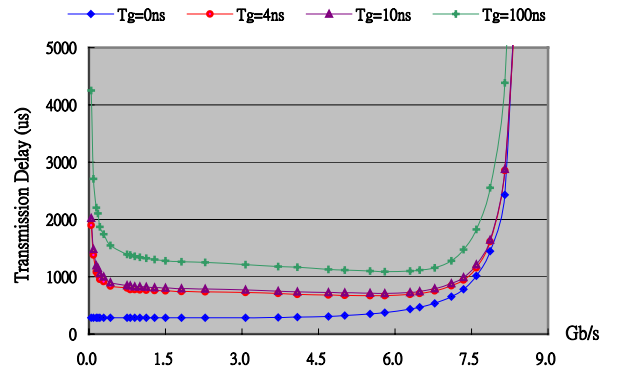
**Fig. 2. Throughput per Node with  $T_g=0ns$ ,  $B_{min}=200$  Bytes.**



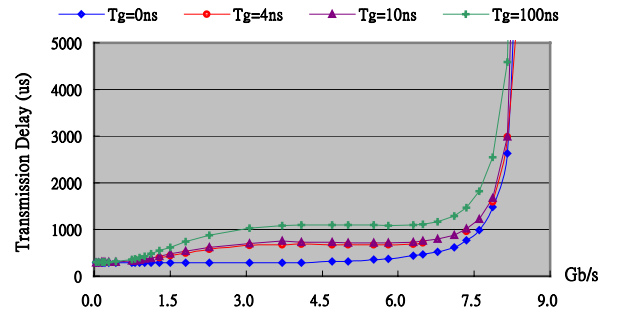
**Fig. 3. The relationship of  $P_{max}$  between  $B_{max}$  and  $B_{min}$  under different  $T_g$  values with  $W=4$  and  $N=8$ .**



**Fig. 4. The relationship of  $P_{max}$  between  $B_{max}$  and  $B_{min}$  under different  $T_g$  values with  $W=8$  and  $N=8$ .**



**Fig. 5. The average transmission delay under different  $T_g$  with no time assembly scheme.**



**Fig. 6. The average transmission delay under different  $T_g$  with added time assembly scheme.**

## References

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